

# KYLE FELIX

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## EXPERIENCE

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June 2016-  
September 2020  
(3 Summers)

Pathways Intern (Engineering), Naval Air Warfare Center Weapons Division (NAWC-WD),  
China Lake, CA

- Worked on aircraft avionics box testbenches, assisted a System Engineer in a weapons system simulator upgrade by researching VME modules, and updated cable schematics in OrCAD.
- Assisted with thermal battery heat analysis tests. Data reduction was accomplished via DIAdem. Additional methods of automating data reduction were explored in LabVIEW.
- Electromechanical technician training in soldering and creating cables from schematics.
- Tier 3 Secret Clearance

Jan 2020-Present

Undergraduate Teaching Assistant, Oregon State University

- As a TA for ENGR 201, I am required to have a deep understanding of circuit analysis techniques as well as excellent public speaking and interpersonal communication skills so that I can be effective leading recitations and review sessions.

## EDUCATION

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Oregon State  
University  
2016-2020

Bachelor of Science in Electrical & Computer Engineering

- Minor: Computer Science
- GPA: 3.85
- Applicable Classes: Microcontroller System Design, VLSI System Design, Digital Logic Design, Computer Architecture, Digital Signal Processing, Computer Organization & Assembly Language, Electronics I-II and Signals & Systems I-III.

## KEY SKILLS

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- Embedded C
- HDL (System Verilog & Verilog)
- MATLAB
- ModelSim
- OrCAD
- LabVIEW
- DIAdem
- C++
- AutoCAD Eagle
- Spice
- Python
- Technical Writing
- Public Speaking
- Linux/Vim
- Soldering
- Libero SoC v12.3

## PROJECTS

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Tachyon FPGA Card  
2019 – 2020  
(7 months)

- VNX form-factor FPGA designed using the Microsemi PolarFire MPF300TS SoC. Using an existing design, my team removed unnecessary components and altered the remaining connections to fit with the VNX Standard for size and connector pinout. The purpose of the FPGA card was to implement 8 differential high-speed transceivers on a compact PCB. This project was sponsored by Praesum Communications.

Arduino Compatible  
Devboard

- In this personal project, I designed small formfactor, AtMega32U4 based, microcontroller board. This project included researching components, reading and understanding datasheets, schematic design, and PCB layout.

GitHub

- This GitHub link shows some of my relevant work with 8-bit microcontrollers. Lab6.c is a multipurpose alarm clock with local and remote temp sensors with an FM radio. <https://github.com/felixky/ECE473>