

Zach Steinberg

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Education

Oregon State University — Corvallis, OR

Expected Graduation: Winter 2021

Honors B.S. Electrical and Computer Engineering GPA: 3.88

Member of Tau Beta Pi and Eta Kappa Nu Honors Societies

Professional Experience

Hardware Engineering Intern - Biamp Systems (MECOP)

2019

Beaverton, OR

- PCB Design (schematic and layout)
 - Small cable adapter (RJ45 to 4 pin Phoenix contact)
 - Switching resistive load (large board, microcontroller, and complex relay configuration)
 - Pink noise generator (analog design)
- Researched current sensing component alternatives and retrofitted existing products with new sensors
 - Worked with suppliers and gave multiple presentations on testing results of new sensors
 - Updated schematics of existing products with alternate sensors for future use

Technical Assistant - Oregon State University

2017-2020

Corvallis, OR

- Led lab sections and held office hours for students in the introductory electrical and computer engineering courses
- Helped students learn technical skills through projects assigned in lab

Customer Service Intern - Custom Decorators Inc.

2016 - 2018 (While not in school)

Barista - Starbucks

2014-2016

Technical Skills

- Embedded C, C++, Linux, Vim, Terminal Commands, MATLAB, Spice, Github
- Soldering, circuit design/assembly
- Use of Arduino and Raspberry Pi
- Schematic and Layout Design with Altium Nexus, OrCad, and Autodesk EAGLE
- Audio Precision software and equipment
- Customer service, point of sale system, Salesforce software, technical writing, public speaking

Projects

Junior Design Project

- Remote AC switch 2018-2019
 - Hosted a web server on a Raspberry Pi 0W to control 2 outlets. Wrote an algorithm on an Arduino to measure the current through each channel, and interfaced the Raspberry Pi and Arduino to display current levels on the web page hosted by the Raspberry Pi.

Senior Design Project

- Tachyon FPGA Card- Sponsored by Praesum Communications 2019-2020
 - Re-designed existing PolarFire splash kit to fit constraints of Space VNX standard (VITA 74), including changing PCI-E to 400 pin connector, removing unnecessary components, changing to VNX dimensions, and changing connections

Microcontroller System Design Project

- Fully functioning alarm clock with FM radio and temperature sensors, using UART, SPI, TWI 2019
 - Programmed ATMEGA128 microcontroller, created schematic for interfaces with microcontroller, programmed ATMEGA168 microcontroller for use with a remote temperature sensor

Other Projects

- POV display wand, RGB audio visualizer

Applicable Major-Related Course Work

Computer Architecture and Assembly

CMOS Integrated Circuits

Digital Logic and Design

Digital Signal Processing

Electronic Materials and Devices

Electronics 1 and 2

Engineering Design Project

Junior Design

Microcontroller System Design

Signals and Systems

Transmission Lines