Vladimir Vesely

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EDUCATION

Bachelor of Science, Electrical Engineering

Oregon State University: Cumulative GPA 3.70

Associate of Science

Portland Community College

Early College High School program - Graduated with highest honors.

Relevant Classes

- $\bullet\,$ ECE 416 & 417 Materials and Devices
- ECE 522 & 523 CMOS
- ECE 473 Microcontroller System Design

NOTABLE PROJECTS

Coded Data Transmission and Display for Sap Flow Measurement Capstone

- Coded Feather M0 to communicate with server via Ethernet & HTTP Requests
- $\bullet\,$ Used PDO PHP to handle MySQL database requests and modification
- Created website for sap flow data display using asynchronous Javascript, HTML, and chart.js

Wrote Cellular Interface Libraries for Electric Fence Monitoring System

- Programmed ESP-32 micro-controller to control cellular module using AT commands via UART
- Programmed Linux-based server to control cellular module using AT commands via USB

Designed Power Supply for Electric Fence Monitoring System

- Chose topology, selected parts, and laid out PCB using KICAD
- Wrote test procedure and verified device operation via oscilloscope
- Designed a filter targeting high frequency switching noise to isolate analog power line

Designed Processor for ECE 271

- Researched processor architectures, Verilog standards, verification software
- Created and implemented 8 command Instruction Set Architecture in Verilog

WORK EXPERIENCE

Building Technologies Engineering Intern

Siemens

• Debugged HVAC PID controller hardware

• Wrote python scripts to automate documentation and panel debugging

Academic Learning Assistant (ALA)

 $Oregon\ State\ University$

• Student-Staff position helping students learn how to learn through thoughtful individual conversations, presentations, and passive educational programming

• Met ALA yearly helping interaction targets within 5 weeks in dorm of 550 engineering students

SKILLS

Programming Experience C++, C, AVR Assembly, Bash, Python, Matlab, Javascript, PHP

Hardware Soldering, PCB Design, Oscilloscope Usage

Circuit & Device Simula- Cadence Virtuoso, LTspice, HSPICE, Atlas TCAD

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Expect. 2020

2015-2017

Summer 2019

2018-2020