Irene George

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EDUCATION

Oregon State University   Corvallis, OR
Bachelor of Science in Electrical and Computer Engineering [GPA: 3.86]   *Sept. 2020 – June 2024*
- Minor in Computer Science

RELEVANT COURSEWORK

Analog and Digital Communication, Power Electronics, Energy-Efficient VLSI Design, Computer Architecture

TECHNICAL SKILLS

Programming Languages: Python, C++, C, OpenGL, Verilog

Other: Soldering, Cadence, LTSpice, Fusion360, Matlab/Simulink, KiCAD

EXPERIENCE

Electrical Engineering Intern   Apr. 2023 – May 2024
Jacobs Engineering   *Corvallis, OR*
  * Reviewed over 30 equipment submittals for motor control centers, transformers, and variable frequency drives
  * Performed lighting calculations using AGI32 to determine quantity and alignment of lighting fixtures in rooms
  * Developed one-lines for wastewater treatment plants using SKM Power Tools
  * Used SolidWorks to layout a programmable logic controller
  * Created drawings to route power and network lines

Circulation Assistant   Aug. 2021 – Mar. 2023
Valley Library, Oregon State University   *Corvallis, OR*
  * Assisted 50 patrons a day with accessing materials and equipment

eCommerce Clerk   May 2020 – Aug. 2021
Fred Meyers   *Tualatin, OR*
  * Organized and gathered products for customers' online orders

PROJECTS

AudioLux Capstone: Worked independently and as a team to upgrade LED audio visualizer. Designed mono mix and DC shift circuits for audio processing. Used Fusion360 to design enclosure for PCB. Used oscilloscope and digital multimeter for testbenching. Assumed role of team lead.

Single Arm SCARA Robot: Worked independently and as a team to build a SCARA robot to create drawings based on user input. 3D modeled and assembled system. Assisted with PCB design and circuit assembly.

Jumping Sprite: Used Quartus Prime to develop a Verilog program to control a moving sprite based on inputs from an FPGA