Carter Ferrazzano

1828 SE Quail Cir, Hillsboro, Oregon 97123

☎ 503-858-0362   ✉ carter.ferrazzano@gmail.com

Education

Oregon State University	**Sep. 2022 – Present**

Bachelor of Science in Electrical and Computer Engineering - Expected Graduation June 2026   *Corvallis, Oregon*
Current GPA: 3.94

Skills

Programming Languages: C, C++
Technical Skills: VS Code, Rhinoceros 3D, Quartus Prime, Model Sim, Circuit Analysis, Physics, Advanced Mathematics, Signal Analysis
Soft Skills: Effective Communication, Ready to Work in a Team, Goal-oriented, Reliable

Experience

Actionlink	**July 2023 – August 2023**

Intel Sales Representative at Best Buy
- Offered knowledge to customers about a variety of Intel processor-based laptops and desktop computers.
- Demonstrated capabilities of Intel processor-based laptops and desktop computers, depending on needs of customers.
- Participated in various training events to increase knowledge of these Intel based systems.
- Worked well with the Best Buy employees to best suit customer needs and answer any questions.
- Exhibited responsibility and adaptability when helping customers with various issues.
- Set up exhibits of new Intel processor-based laptops to show customers how systems can benefit users.

Projects

BMO-scope | *Design, Build, Test*   **2025**
- Collaborated with two other team members to design and build a Teensy based oscilloscope that sampled at 200 kHz, measuring frequency, volt peak-to-peak, period, and had a configurable trigger.
- Utilized data sheets and other online resources to find suitable inputs for the signals and the rotary encoders.
- Tested all of the inputs using Arduino IDE coded in C.
- Achieved the Technical Choice award directly from the instructors for overall design and build quality.

Summation Calculator | *Quartus Prime, ModelSim*   **2024**
- Developed a summation calculator that would take up to a positive 4-bit number and sum that number along with each lower value until it reaches zero with Quartus Prime.
- Created a block diagram for each task that needed to be completed in calculator.
- Tested calculator inside of ModelSim and on an FPGA.

Alarm Clock | *Quartus Prime, ModelSim*   **2024**
- Developed an alarm clock that allows users to set a time, in hours and minutes. Alarm clock could be enabled or disabled after it is set. Once time is reached, different signals are sent out to prove alarm is going off.
- Tested alarm clock using ModelSim and on an FPGA

Gasoline Engine Go Kart | *Design, Build, Test*   **2022**
- Operated within a team of five. Responded to group messages, thoughts, and ideas almost immediately in a polite and respectful manner.
- Researched different options and methods for design and construction.
- Designed using Rhinoceros 3D.
- Constructed frame (welded 1¼" square tubing), brakes, safety systems, steering, pedals, wheels, electrical system, and aesthetics.

Awards

Finley Academic Excellence Scholarship	2022 - 2026	Intel Gordon Moore Scholarship	2023 - 2024
Student Success Initiative Scholarship	2022 – 2026	Tony Platt Forever Memorial Scholarship	2024 - 2025