Christian Ovchinikov

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EDUCATION

Oregon State University   Corvallis, OR
Bachelor of Science | GPA: 3.3/4.0   *Expected Graduation: June 2023*
Major in Electrical and Computer Engineering, Minor in Computer Science

EXPERIENCE

General Contractor/Laborer   June 2016 – Present
N.W. Contractors Inc.   *Salem, OR*
- Lead a team to complete tasks provided by the general contractor
- Framing, siding, painting, plumbing, electrical, ordering material, etc.
- New construction and renovations, commercial and residential
- Communicate with customers to ensure customer satisfaction

Chief Executive Officer   Dec. 2020 – Present
Dynamic InfoTech Inc.   *Salem, OR*
- Ensure customers have reliable computer hardware and software
- Provide diagnosis and repair of faulty computer hardware and software
- Provide quality customer service and ensure customer satisfaction

Kitchen Lead   Sep. 2013 – Mar. 2018
Rock-N-Rogers   *Salem, OR*
- Ensure the kitchen operated smoothly and leading the kitchen staff
- Cooking, prepping, sanitizing the kitchen, ordering inventory, receiving of inventory, making schedules, managing money, etc.
- Would also take orders and serve during slow times

SCHOOL PROJECTS

Contact-less Infrared Temperature Sensor | *Arduino IDE, Python, HTML, Blender*   Jan. 2022 – Mar. 2022
- In response to the COVID pandemic, the sensor was designed to be used by the user without any physical contact with the device
- Utilized a QR code that allowed the user to use their smartphone to start the device
- Used APIs in HTML and Python along with serial communication to communicate between the remote Debian server, the external database, and the device itself.
- A 3D enclosure was designed using Blender and then printed at the university
- Junior Design Project Showcase Link

Note Visualizer for Junior Design | *EasyEDA, LTSpice, Arduino IDE, MATLAB*   Sept. 2020 – Dec. 2020
- Designed a note visualizer for notes inclusively between C4 and C5
- Designed the circuit using EasyEDA, then assembled the hardware
- Code was created that used Fourier transforms to detect notes in the specified frequency range
- Analysis was done using MATLAB in order to test the functionality and efficiency of the circuit

Digital Timer Program for Max10 FPGA | *Quartus Prime, SystemVerilog, ModelSIM*   May 2020 – June 2020
- Coded SystemVerilog files in Quartus Prime
- Ran tests that the program was stable in ModelSIM
- Programmed the Max10 FPGA on a DE10-Lite with Quartus Prime
- The timer worked on the board with the LED's and Switches

TECHNICAL SKILLS

Languages: C/C++, DOS batch, Ruby, SystemVerilog, Java, Python, HTML/CSS
Software: Solidworks, Inventor, AutoCAD, EasyEDA, QTCreator, LTSpice, Quartus Prime, ModelSIM, MATLAB, Git, Visual Studio, Wireshark, Adobe Photoshop and Illustrator, Cinema4D, Microsoft Office
Tools: Oscilloscope, Digital Multi Meter, Various construction tools
Misc: Quick learner, Versatile, Self-driven and works well with teams, Ability to Multi-task