

# Blake Aaron Roberts

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## OBJECTIVE

To obtain a summer graduate internship to further develop the technical skills necessary to enter industry as an analog and mixed-signal design engineer upon completion of master's degree.

## EDUCATION

<b>Oregon State University</b>	2023 - Present
Master of Science - Electrical and Computer Engineering	
• Focus: Analog Integrated Circuits	
• Accelerated Master Platform	
• Expected Graduation: June 2025	
<b>Oregon State University</b>	2021 - Present
Bachelor of Science - Electrical and Computer Engineering	GPA: 3.81
• Minor: Computer Science	
• Expected Graduation: June 2024	
<b>Chemeketa Community College</b>	2019 - 2021
92 Undergraduate Credit Hours	GPA: 4.0

## WORK EXPERIENCE

<b>Naked Extracts - Co-Owner, Lab Manager</b>	2014 - 2019
• Developed and organized business structure and obtained OLCC licensure.	
• Coordinated construction of laboratory facility according to OLCC guidelines.	
• Created high-efficiency standard operating procedures for extraction and isolation of organic compounds.	
• Managed team of 10+ employees in laboratory operations.	
<b>Various Restaurants - Server, Bartender</b>	2009 - 2014
<b>Passey Advertising - Intern, Web Developer</b>	2006 - 2008
• Interned during third and fourth years of high school. Hired full-time upon graduation.	
• Developed full-stack websites for clients using LAMP stack.	

## PROJECTS

<b>ESP32 Microcontroller Breakout Board</b>	
Engineered and assembled a printed circuit board (PCB) for an ESP32 breakout module.	
<b>PC Controlled DC Power Supply</b>	
Worked with a team to engineer a two-channel, variable voltage, benchtop DC power supply, programmable using SCPI commands via computer interface.	
<b>3-Filter Crossover Network</b>	
Designed and constructed a three-way crossover network of AC filters to drive a multiple speaker system.	

## ACADEMIC ACCOMPLISHMENTS

<b>Engineering Honor Society Officer</b> - Secretary, Tau Beta Pi (Oregon Alpha Chapter)	Fall 2023 - Present
<b>Dean's List</b> - Oregon State University	Fall 2022
<b>President's List</b> - Chemeketa Community College	Fall 2020 - Spring 2021

## SKILLS

General:	Analog Circuit Design, Circuit Analysis, Circuit Optimization, Schematic Design, PCB & IC Layout, Simulation, Modeling, Filter Design, Amplifier Design, Sensitivity Analysis, Documentation, Teamwork, Communication, Problem-Solving, Time Management, Technical Writing, Continuous Learning
Software:	Cadence Virtuoso, KiCAD, MATLAB, SPICE, Quartus Prime, Microchip Studio, Visual Studio Code
Languages:	C, C++, Python, SystemVerilog, AVR Assembly, L <sup>A</sup> T <sub>E</sub> X