

KHANH LE

+1(425) 903-7344 ◇ Corvallis, OR ◇ lebaokhanhh@gmail.com ◇ linkedin.com/in/khanh-le-bao/

EDUCATION

Bachelor of Science in Electrical and Computer Engineering

Oregon State University

Expected June 2024

GPA 3.87

Minor: Computer Science

Relevant Coursework: Microcontroller, Computer Architecture, VLSI System Design, Computer Organization and Assembly Languages, Signals and Systems, Electronics, CMOS, Operating System, Hardware Verification

SKILLS

Software Tools

Quartus Prime, Model Sim, LT Spice, KiCAD, MATLAB, Cadence Virtuoso

Hardware

FPGA, Oscilloscope, Atmega32U4, ATXmega64A4U , PCB Design, Function Generator, DC Power Supply, Multimeter

Coding Languages

C, Assembly, C++, Java, Java Script, Python, Verilog, System Verilog,

Language

English, Vietnamese

EXPERIENCE

EVSE HARDWARE AND SOFTWARE DEVELOPMENT INTERN

June 2022 - December 2022 Daimler

Trucks North America

Portland, OR

- Led training sessions on Electric Vehicle Supply Equipment (EVSE) services for worldwide dealership employees
- Spearheaded implementation of Open Charge Point Protocol (OCPP) 1.6J and OCPP 2.0.1 at department level
- Designed Charging Management System (CMS) marketing initiatives based on competitive analysis
- Identified tools and configuration for EVSE starter kit for EV charging testing in the department
- Managed and tracked orders for 20+ EVSE chargers, ensuring timely delivery and accurate invoicing

UNDERGRADUATE LEARNING ASSISTANT

January 2022 - present

Oregon State University College of Engineering

Corvallis, OR

- Led and mentored a team of 6 learning assistants to ensure consistent quality in lab instructions
- Taught multiple weekly Digital Logic Design and Electrical Fundamentals labs of 20+ students
- Aid students in troubleshooting lab challenges that utilize ModelSim, FPGA, LT Spice, SystemVerilog, Oscilloscope, Power Supply, Digital Multimeter, and Function Generator

PROJECTS

Matrix LED Box

Junior Design Project

- Created an LED display to perform custom animation programmed by FPGA on 64 LED bulbs
- Developed a JavaScript-based GUI for collecting user input (text, images, and animations) which is then transferred to FPGA via bluetooth
- Designed, fabricated and tested a 12V to 5V 2-output voltage regulator power supply PCB using KICAD, Oscilloscope.

FPGA Dice Generator

Digital Logic Design

- Created a random number generator to simulate dice behavior utilizing FPGA and Modelsim
- Design algorithm in SystemVerilog that ensures no same digit is generated consecutively

LEADERSHIP AND INVOLVEMENTS

- Member, Society of Hispanic and Professional Engineers September 2023 - Present
- Lead Undergraduate Learning Assistant, Oregon State University January 2022 - March 2022
- Program Coordinator, Cascadia College November 2019 - June 2020