# Tyler Titsworth

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𝗞 Handshake

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**5**03-754-3850

**College of Electrical Engineering and Computer Science** 

# **EDUCATION**

#### **Oregon State University**

BA in Computer Science GPA: 3.22/4.0

# **EXPERIENCE**

#### Intern

#### Data Center Operations

Currently apart of an Internship at Intel that focuses on Data Center Operations for High Performance Computing. Working with scientists and engineers pushing the boundaries of AI and Machine Learning research.

#### **IT Support**

Prostaff IT Student Worker

Experience with Microsoft SCCM and Active Directory to maintain compliance of over 2000+ computers. Work with webpage design and help documentation for an exhaustive library of software like Matlab, arcGIS, and AutoCAD. Act as a technician for a wide range of issues and devices while maintaining university enterprise standards.

#### ECE TA

#### Teaching Assistant for ECE 272

After helping rewrite the curriculum for the Digital Logic Design Lab, I spent a term as a TA attempting to act as a technical support student and teacher for the new software implemented in the course. I presented materials to students and worked hands-on to troubleshoot their FPGA problems. I held office hours where I would help setup Windows VMs on both Mac and Linux machines that would otherwise not support the Intel Quartus software.

#### **UHDS Student Worker**

#### Student Trainer

Elevated position from general student worker to help act as a leader and educator for new employees. The concept I worked in was a late night delivery service with an exceptionally large staff, the expectation being that I be able to handle copious amounts of work under pressure, switch gears to train incoming workers, and know every position in a 9 part cooking-to-delivery process fluently.

# PROJECTS

#### CS Capstone Project: Know It's Off

Redesign and build a smart device that can easily tell a user whether or not their stove is on or not. This project has me working with Electrical Engineers and Computer Scientists to revive a previously failed Capstone project to make a functional UI/UX, power efficient device, that's both adaptable and expandable, along with Google Home support.

#### **Rewriting Digital Logic Design 272 Lab Curriculum**

I and the Lead TA for ECE272 rewrote the entirety of the ECE 272 Lab website curriculum as the class transitioned from Lattice Diamond to Intel Quartus. The new FPGA board and software meant the entire restructuring of the lab curriculum, of which we recreated all of the previous labs in the new software as well as working to come up with new lab ideas like integrating VGA and SPI.

## Skills

Extensive Language Experience	Technologies
• CISC/RISC Assembly	o Linux
• Batch/Bash/Powershell	o Git
• C/C++	o IATEX
• JS/HTML/CSS	• Microsoft SCCM
• Python	• React
General	Hobbies
• Communication	o eSports
• Organization	• Tabletop and Video Games
• Project Management	• Writing

# Expected Graduation: Winter 2022

### **Intel Corporation**

Corvallis, OR

January 2021 - Present

#### **College of Engineering IT**

Sept 2017 – Present

**University Housing and Dining Services** 

Oct 2016 - June 2018

Sept 2020-Present

August 2018

Sept 2018 - Dec 2018