



WANG XIANG

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EDUCATION

Bachelor of Science | *Electrical and Computer Engineering*
Oregon State University

Sep 2018 – Sep 2023
Oregon, US

TECHNICAL SKILLS

Familiar Languages: C++, C, System Verilog

Tools: DMM, Soldering Iron

Software: Modelsim, MATLAB, Quartus, LTSpice, Arduino

WORK EXPERIENCE

Labs works

Do the labs in Junior Design course
* Do lab works and projects with teammates in courses.

Jan 2021 – Mar 2021
Oregon, US

PROJECTS AND RESEARCH

Project about FPGA in Digital logic Design | *Quartus, Modelsim*

Organize teammates to meeting, and determine the project goals.
Oregon State University

Fall 2019

Projects in Junior Design | *MATLAB, LTSPice, Ardunio*

Organize teammates to meeting and work together, and determine the project goals.
Oregon State University

Winter 2021

Projects in Junior Design | *MATLAB, Ardunio*

Create a 3D RGB cube that can change color based on user selection.
Oregon State University

Spring 2021

COMMUNITY INVOLVEMENT

Member in Robotic club

Discuss and practice how to realize the specific function of the robots.

Spring 2021 – Sep 2023
Oregon, US