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Objective:

Gain work experience in the area of processor microarchitecture design & teamwork in an industry environment.

Education/Skills:

LTspice	Digital Logic Design
Circuit Analysis	FPGA experience
C/C++/Python/System Verilog	OOP
Schematic & Board Creation	Transistor networks

Volunteer & Extracurricular Experience:

2013 - 2014: McKay Orchestra Concert helper
 Set up stage
 Help conductors & players find their places

2018 - Present: CS 160/161/162 tutor
Helping peers understand topics covered in beginning CS courses. Taught topics such as recursion, classes, inheritance, if statement logic, arrays, Stack vs. Heap, pointers, etc.

Work Experience:

June – August 2015: TIS

Setting up computers in schools	Troubleshooting
Moving computers to surplus	Network cabinet management
Windows image install	

July – August 2018: Gallon House Nursery

Move plants/tree	Dispose of dead plants
Removing plastic/ covering of green houses	Weeding
Tractor driver	

Projects:

Charging circuit with fault protection & multiple voltage output for a single Li-ion cell

VGA driver implemented on an FPGA with Nintendo NES controller input functionality for color selection

Created a 3 band audio amp